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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
				Application Number	Based on 09/714,891-10/645516
Sheet 1		of 3	Filing Date	November 17, 2000 6/22/03	
			First Named Inventor	Koji ONO et al.	
			Group Art Unit	2815	
			Examiner Name	Jerome Jackson Jr.	
			Attorney Docket Number	0756-7186	

U.S. PATENT DOCUMENTS						
Examiner Initials ¹	Cite No. ¹	U.S. Patent Document		Name of Patent or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
P		5,643,826		Ohtani et al.	07/01/97	
P		5,923,962		Ohtani et al.	07/13/99	
P		5,247,190		Friend et al.	09/21/93	
P		5,399,502		Friend et al.	03/21/95	

FOREIGN PATENT DOCUMENTS								
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		Office ⁴	Number ⁵	Kind Code ² (if known)				
P		JP	07-130652			05/19/95		Abst.
P		JP	08-078329			03/22/96		Abst.
P		JP	10-135468			05/22/98		Abst.
P		JP	10-135469			05/22/98		Abst.
P		JP	10-294280			11/04/98		Abst.
P		JP	11-191628			07/13/99		Abst.
P		JP	10-092576			04/10/98		Abst.
P		JP	11-345767			12/14/99		Abst.
P		JP	11-354442			12/24/99		Abst.
P		pct	WO90/13148			11/01/90		Eng.

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ³
P		Shimokawa et al., "Characterization of High-Efficiency Cast-Si Solar Cell Wafers by MBIC Measurement," Japanese Journal of Applied Physics, Vol. 27, No. 5, May, pp. 751-758, 1988.	
P		Hatano et al., "A Novel Self-Aligned Gate-Overlapped LDD Poly-Si TFT With High Reliability and Performance," IEDM Technical Digest 97, pp. 523-526.	
P		Hermann Schenk et al., "Polymers for Light Emitting Diodes," EuroDisplay '99 Proceedings, pp. 33-37.	

Examiner Signature		Date Considered	2/19/05
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¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	Based on 09/714,891-16455/6
		Filing Date	November 17, 2000-8182103
		First Named Inventor	Koji ONO et al.
		Group Art Unit	2815
		Examiner Name	Jerome Jackson Jr.
Sheet 2 of 3	Attorney Docket Number	0756-7186	

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		Number	Kind Code ² (if known)			
D		4,394,182		Maddox III	07/19/83	
D		5,528,397		Zavracky et al.	06/18/96	
D		5,583,369		Yamazaki et al.	12/10/96	
D		5,686,328		Zhang et al.	11/11/97	
D		5,705,424		Zavracky et al.	01/06/98	
D		5,736,750		Yamazaki et al.	04/07/98	
D		5,830,787		Kim	11/03/98	
D		5,949,107		Zhang	09/07/99	
D		6,001,714		Nakajima et al.	12/14/99	
D		6,030,667		Nakagawa et al.	02/29/00	

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		Office ⁴	Number ⁴	Kind Code ² (if known)				
D D D D D		JP	09-191111			07/22/97	1	Abst.
		JP	09-293600			11/11/97		Abst.
		JP	10-104659			04/24/98		Abst.
		JP	10-233511			09/02/98		Abst.
		JP	04-369271			12/22/92		Full

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ³
D		Specifications and Drawings for Application Serial No. 09/619,732, "Semiconductor Device and Manufacturing Method Thereof," Filed on July 19, 2000.	
D		Specifications and Drawings for Application Serial No. 09/618,930, "Semiconductor Device and Manufacturing Method Thereof," Filed on July 18, 2000.	
D		Specifications and Drawings for Application Serial No. 09/433,705, "Method of Fabricating a Semiconductor Device," Filed on November 4, 1999.	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	Based on 097714,891/0/64546
		Filing Date	November 17, 2000 2/22/03
		First Named Inventor	Koji ONO et al.
		Group Art Unit	2815
		Examiner Name	Jerome Jackson Jr.
		Attorney Docket Number	0756-7186
Sheet	3	of	3

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		Number	Kind Code ² (if known)			
✓		5,323,042		Matsumoto	06/21/94	
✓		6,198,133	BI	Yamazaki et al.	03/06/01	
✓		6,281,552	BI	Kawasaki et al.	08/28/01	
✓		6,166,414		Miyazaki et al.	12/26/00	
✓		5,393,682		Liu	02/28/95	
✓		5,100,820		Tsubone	03/31/92	
✓		6,534,826		Yamazaki	03/18/03	

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		Office ⁴	Number ⁴	Kind Code ² (if known)				
✓		JP	05-102483			04/23/93		Full
✓		JP	8-274336			10/18/96		Full
✓		JP	7-235680			09/05/95		Full
✓		JP	6-148685			05/27/94		Full
✓		EP	1 003 223		Yamazaki	05/24/00		Eng.
✓		EP	1 049 167		Yamazaki	11/02/00		Eng.

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